

DISPLAY PANEL AND METHOD OF PRODUCING THE SAME

CROSS REFERENCE OF RELATED APPLICATION

This application is based on and claims priority under
5 35 U.S.C. §119 with respect to Japanese Patent Application
No. 2002-188940 filed on June 28, 2002, the entire content
of which is incorporated herein by reference.

BACKGROUND OF THE INVENTION

10 The present invention relates to a display panel such
as a plasma display panel and a method of producing the display
panel.

A display such as a plasma display panel (hereinafter
called the PDP) using glass substrates is formed by putting
15 two sheets of glass substrates on top of each other with a
predetermined space formed therebetween in order to seal
predetermined structural components inside. The structure of
a display area (display cell structure) where an image is displayed
will be described hereunder by reference to one example already
20 proposed by the present applicant.

Fig. 1 is an exemplary plan view of the display cell
structure of the PDP; Fig. 2, a sectional view taken on line
V1 - V1 of Fig. 1; Fig. 3, a sectional view taken on line V2
- V2 of Fig. 1; Fig. 4, a sectional view taken on line W1 -
25 W1 of Fig. 1; and Fig. 5, a sectional view taken on line W2

- W2 of Fig. 1.

As shown in Figs. 1 to 5, a plurality of line electrode pairs (X and Y) are arranged in parallel so as to extend in the line direction (lateral direction of Fig. 1) of a front substrate 10 on the back of the front substrate 10 as a display surface in the display area of the PDP. Each line electrode X is formed with a transparent electrode Xa such as a T-shaped transparent conductive film of ITO (Indium Tin Oxide) and a bus electrode Xb of a metal film extended in the line direction of the front substrate 10 and connected to the narrow base end portion of the transparent electrode Xa.

Like the line electrode Y, each line electrode Y is formed with a transparent electrode Ya such as a T-shaped transparent conductive film of ITO (Indium Tin Oxide) and a bus electrode Yb of a metal film extended in the line direction of the front substrate 10 and connected to the narrow base end portion of the transparent electrode Ya.

The line electrodes X and Y are arranged alternately in the column direction (vertical direction of Fig. 1) of the front substrate 10. The pair of transparent electrodes Xa and Ya arranged in a row along the bus electrodes Xb and Yb are respectively extended in the directions of the opposite line electrodes. Moreover, the broad top side portions of the transparent electrodes Xa and Ya face each other with a predetermined discharge gap g held therebetween.

The bus electrodes Xb and Yb are formed into a two-layer structure having black conductive layers Xb1 and Yb1 on the display surface side and main conductive layers Xb2 and Yb2 on the back side. On the back of the front substrate 10, a
5 black light-absorption layer (shield layer) 30 extending in the line direction along the bus electrodes Xb and Yb is formed between the bus electrodes Xb and Yb sitting back to back with respect to the adjoining line electrode pair (X and Y) arranged in a row direction. Further, a light absorption layer (shield
10 layer) 31 is formed in a portion opposite to the vertical wall 35a of each partition wall 35.

On the back of the front substrate 10, a dielectric layer 11 is formed so as to cover the line electrode pairs (X and Y) and on the back of the dielectric layer 11, a bulked dielectric
15 layer 11A protruding from the back of the dielectric layer 11 is formed so as to extend in parallel to the bus electrodes Xb and Yb in a position opposite to the bus electrodes Xb and Yb adjacent to the adjoining line electrode pair (X and Y) and in a position opposite to the area between the bus electrode
20 Xb and the bus electrode Yb adjacent to each other.

A protective layer 12 of MgO is formed on the back side of the dielectric layer 11 and the bulked dielectric layers 11A. On the other hand, column electrodes D are arranged in parallel at predetermined intervals on the display-side surface
25 of a back substrate 13 arranged in parallel to the front substrate

10 so that each column electrode D is extended in a direction
perpendicularly crossing the line electrode pair (X and Y) (in
the column direction) in a position opposite to the pair of
transparent electrodes Xa and Ya of the line electrode pair
5 (X and Y). On the display-side surface of the back substrate
13, further, a white dielectric layer 14 for covering the column
electrodes D is formed and the partition walls 35 are formed
on the dielectric layers 14.

The partition walls 35 are formed into the shape of a
10 lattice by the vertical wall 35a extending in the column direction
in a position between the column electrodes D arranged in parallel
to each other and the horizontal wall 35b extending in the line
direction in a position opposite to the bulked dielectric layer
11A. With the latticelike partition walls 35, the space between
15 the front substrate 10 and the back substrate 13 is formed into
sections, in each of which the transparent electrodes Xa and
Ya are placed opposite to each other in the line electrode pair
(X and Y) to form a discharge space S.

The display-side face of the vertical wall 35a of each
20 partition wall 35 is not in contact with the protective layer
12 (see Fig. 4) and a space γ is formed therebetween. However,
the display-side face of the horizontal wall 35b is brought
into contact with a portion of the protective layer 12, the
portion being used to cover the bulked dielectric layer 11A
25 (see Figs. 2 and 5), so that adjoining discharge spaces S in

the column direction are shielded from each other.

A phosphor layer 16 is formed on the vertical walls 35a and the sides of the horizontal walls 35b of the partition walls 35 facing each discharge space S and the surface of the dielectric 5 layer 14 in such a manner as to cover all of these five sides in due order.

The color of the dielectric layers 16 is set R, G and B in the line direction successively in the respective discharge spaces S (see Fig. 4). Moreover, rare gas is enclosed in the 10 discharge spaces S. The horizontal walls 35b1 and 35b2 of each partition wall 35 that separates the discharge space S from another are separated from each other in the column direction by a space SL provided in a position where the light absorption layer 30 between display lines is placed above the space SL.

15 More specifically, the partition walls 35 are formed into the shape of a lattice along the display lines L and arranged in parallel to each other via the space SL extending along the display lines L in the column direction. The width of the space SL is set so that the width of the portions 35b1 and 35b2 of 20 the horizontal wall 35b separated from each other by the space SL provided between the display lines L becomes substantially equal to the width of the vertical wall 35a.

In the PDP above, the line electrode pair (X and Y) forms one display line (line) L of a matrix display screen and each 25 of the discharge spaces S separated from each other by the

latticelike partition wall 35 is used to determine one subdivided discharge cell C.

The PDP above is produced by laying the front substrate formed with the line electrode pairs, the dielectric layer, 5 the bulked dielectric layers and the protective layer upon the back substrate formed with the column electrodes, the protective layer of the column electrodes, the partition walls and the phosphor layers; by sealing the surrounding of the combination of the substrates; forming a vacuum in the interior space 10 therebetween; and enclosing a discharge gas therein.

However, in case where the structural components formed on both the front and back substrates cause a relative deviation in position when the substrates are stuck together, a normal electric discharge is impeded and good display quality is 15 unavailable. Consequently, positioning marks have been formed outside the display area of the PDP on the substrates whereby to carry out the positioning of both the substrates relatively.

Fig. 6 is a diagram illustrating conventional positioning-mark forming layers. In the display area of the PDP, a cross 20 section taken on line from V1 up to W1 and perpendicularly folded at K of Fig. 1 shows the relation between each layer within the display area and the positioning-mark forming layers formed outside the display area.

As shown in Fig. 6, metal films as bus marks Mb are formed 25 on the outer side 10b of the display area of the front substrate,

the bus marks Mb being formed in the same layer as a layer in which the bus electrodes Xb and Yb are formed within a display area 10a. Further, metal films as address marks Ma are formed on the outer side 13b of the display area of the back substrate,
5 the bus marks Ma being formed in the same layer as a layer in which the column electrodes D are formed.

Heretofore, the bus marks Mb and the address marks Ma have been used as positioning marks. As the bus marks Mb and the address marks Ma are formed of metal films, the relative
10 positions of the marks are made detectible by the use of transmissive illumination, so that it is possible to carry out the positioning of the bus electrodes Xb and Yb and the column electrodes D that are formed in the respective mark-positioning layers.

15 With respect to influence over the performance of the PDP affected by relative deviation in position as mentioned above, importance is directed to the positional accuracy of the transparent electrodes Xa and Ya projected opposite to each other via the discharge gap on a cell basis against the partition wall (especially the vertical wall 35a) or the positional accuracy
20 of the bulked dielectric layer 11A against the partition wall (especially the horizontal wall 35b).

The transparent electrodes Xa and Ya and the bulked dielectric layers 11A are formed on the front substrate 10 with
25 the bus marks Mb as a reference, whereas the partition walls

35 are formed on the back substrate 13 with the address marks Ma as a reference. Therefore, deviation in position caused at the step of formation or deviation in position caused at the step of calcination under the influence of reduction in 5 the size of the substrate tends to occur against each of the marks.

In the PDP formed by carrying out the positioning of both the substrates relatively based on the bus marks Mb and the address marks Ma and sticking the front substrate 10 and the 10 back substrate 13 together, such deviation in position is liable to occur between the transparent electrodes Xa and Ya and the partition wall (vertical wall 35a) or the bulked dielectric layer 11A and the partition wall (horizontal wall 35b).

Further, the influence of deviation in position increases 15 when the display cells are reduced in size so as to cope with sophistication of the PDP, the problem is that the performance of the PDP lowers.

SUMMARY OF THE INVENTION

An object of the invention made to solve the foregoing 20 problems is to provide a display panel designed to improve accuracy in putting substrates on top of each other and to prevent performance from lowering because of deviation in position.

In order to accomplish the object, according to one aspect 25 of the invention, there is provided a display panel including

a first substrate with transparent display electrodes disposed for forming display cells within a display area, a second substrate disposed separately from and opposite to the first substrate and formed with partition walls for forming sections of the
5 display cells within the display area, a first positioning mark disposed in at least two or more positions outside the display area of the first substrate, and a second positioning mark disposed in at least two or more positions outside the display area of the second substrate, wherein the first positioning marks and
10 the second positioning marks are disposed so that the positional relation between the transparent electrodes and the partition walls can directly be recognized.

In addition, according to a second aspect of the invention, there is provided a display panel including a first substrate
15 with first partition walls disposed for forming sections of transparent display electrodes for forming display cells and the display cells in an at least first direction, a second substrate disposed separately from and opposite to the first substrate with second partition walls disposed for forming
20 sections of the display cells in the at least first direction within a display area, a first positioning mark disposed in at least two or more positions outside the display area of the first substrate, and a second positioning mark disposed in at least two or more positions outside the display area of the
25 second substrate, wherein the first positioning marks and the

second positioning marks are disposed so that the positional relation between the first partition walls and the second partition walls can directly be recognized.

In addition, according to a third aspect of the invention,

5 there is provided a method of producing a display panel comprising the steps of putting a first substrate and a second substrate on top of each other, the first substrate being formed with transparent display electrodes disposed for forming display cells within a display area, the second substrate being formed
10 with partition walls for forming sections of the display cells within the display area, and forming pairs of first positioning marks and second positioning marks for use in verifying the positioning of the transparent electrodes and the partition walls in at least two or more positions outside the display
15 areas of the first substrate and the second substrate before the step of putting the substrates on top of each other.

In addition, according to a fourth aspect of the invention,

there is provided a method of producing a display panel comprising the step of putting a first substrate and a second substrate
20 on top of each other, the first substrate being formed with first partition walls disposed for forming sections of transparent display electrodes for forming display cells within the display area and the display cells in an at least first direction, the second substrate being formed with second
25 partition walls disposed for forming sections of the display

cells within the display area, and forming pairs of first positioning marks and second positioning marks for use in verifying the positioning of the first partition walls and the second partition walls in at least two positions outside the
5 display areas of the first substrate and the second substrate before the step of putting the substrates on top of each other.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects and advantages of this invention
10 will become more fully apparent from the following detailed description taken with the accompanying drawings in which:

Fig. 1 is an exemplary plan view of the display cell structure of PDP;

Fig. 2 is a sectional view taken on line V1 - V1 of Fig.
15 1;

Fig. 3 is a sectional view taken on line V2 - V2 of Fig.
1;

Fig. 4 is a sectional view taken on line W1 - W1 of Fig.
1;

20 Fig. 5 is a sectional view taken on line W2 - W2 of Fig.
1;

Fig. 6 is a diagram illustrating conventional positioning-mark forming layers;

Fig. 7 is a diagram illustrating positioning-mark forming
25 layers of a display panel according to the first embodiment

of the invention;

Figs. 8A and 8B are plan views showing exemplary positions 8A where first positioning marks are formed and positions 8B where second positioning marks are formed according to first 5 and second embodiment of the invention;

Figs. 9A and 9B are diagrams illustrating modified examples of configurations of the positioning marks combined;

Fig. 10 is a diagram illustrating Steps S1 to S3 in putting substrates on top of each other in the method of producing display 10 panels according to the first and second embodiments of the invention;

Fig. 11 is a diagram illustrating Step 2 in putting the substrates on top of each other in the method of producing the display panels according to the first and second embodiments 15 of the invention, including an exemplary sectional view of the first positioning mark irradiated with falling illumination.

Fig. 12 is a diagram illustrating Step 3 in putting the substrates on top of each other in the method of producing the display panels according to the first and second embodiments 20 of the invention, including an exemplary sectional view of the second positioning mark irradiated with transmissive illumination;

Fig. 13 is a diagram illustrating Step 4 in putting the substrates on top of each other in the method of producing the 25 display panels according to the first and second embodiments

of the invention;

Fig. 14 is a diagram illustrating positioning-mark forming layers of a display panel according to the second embodiment of the invention.

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

A description will now be given of embodiments of the invention with reference to the drawings.

(First Embodiment)

10 A display panel according to a first embodiment of the invention is similar in structure to the PDP shown in Figs. 1 to 5 excepting a front substrate 10 (first substrate) with transparent electrodes Xa and Ya disposed for displaying purposes, a back substrate 13 (second substrate) formed with partition walls 35 for forming display cell sections in a display area 10a disposed separately from and opposite to the front substrate 10, and positioning marks (first positioning marks M1 and second positioning marks M2) for use at the step of putting both the substrates on top of each other.

20 The structure of the positioning marks for use at the step of putting the substrates on top of each other concerning the display panel according to the first embodiment of the invention will be described hereunder by reference to Figs. 7 to 9B.

25 Fig. 7 is a diagram illustrating positioning-mark forming

layers of the display panel according to the first embodiment of the invention. In the display area of the PDP, a cross section taken on line from V1 up to W1 and perpendicularly folded at K of Fig. 1 shows the relation between each layer within the 5 display area and positioning-mark forming layers formed outside the display area.

In the display panel according to the first embodiment of the invention, the first positioning marks M1 are formed on the outer side 10b of the display area of the front substrate 10 and in the same layer as a layer in which the transparent display electrodes Xa and Ya are formed within the display area 10a as shown in Fig. 7; moreover, the second positioning marks M2 are formed on the outer side 13b of the display area of the back substrate and in the same layer as a layer in which the 15 partition walls 35 are formed within the display area 10a.

Positions where the first positioning marks and the second positioning marks are formed will now be described. Figs. 8A and 8B are plan views illustrating the positions where the first positioning marks and the second positioning marks are formed 20 by way of example.

As shown in Fig. 8A, a columnar ITO mark M1 (the first positioning mark) having an outer diameter of 0.5 mm, for example, is formed at four corners on the outer side 10b of the display area of the front substrate at the same step as that of forming 25 the transparent electrodes Xa and Ya, the ITO marks M1 being

made of the same material as the material of transparent conductive films of ITO. In this case, no dielectric layers are formed on the respective ITO marks M1.

As shown in Fig. 8B, further, a cylindrical rib mark M2 (the second positioning mark) having an outer diameter of 1.5 mm and an inner diameter of 0.8 mm, for example, is formed at four corners on the outer side 13b of the display area of the back substrate at the same step as that of forming the partition walls 35, the rib marks M2 being made of the same material as the material of glass layers containing white pigments.

As described above, the combination of the ITO mark M1 and the rib mark M2 placed opposite to each other has a common center of gravity or a common center as shown in Figs. 8A and 8B, so that their configurations are prevented from overlapping each other. Moreover, the forms of the ITO mark M1 and the rib mark M2 are not limited to be circular but may be elliptical, triangular, rectangular or polygonal.

As shown in Figs. 9A and 9B, further, whether figures combined together in a cross intersect each other at one point (see Fig. 9A) or whether arcs of semicircles combined together form a circle (see Fig. 9B) may be used to form the ITO marks M1 and the rib marks M2 so that the positional relation therebetween is made recognizable by the figures resulting from joining the combination of the opposite marks.

As described above, the display panel according to the

first embodiment of the invention has the ITO mark M1 (first positioning mark) disposed in at least two or more positions on the outer side 10b of the display area of the front substrate and the rib mark M2 (second positioning mark) disposed in at 5 least two or more positions on the outer side 13b of the display area of the back substrate.

With the ITO marks M1 (first positioning marks) and the rib marks M2 (second positioning marks) thus structured and disposed as shown in Figs. 7, 8A and 8B above in the display 10 panel according to the first embodiment of the invention, the positional relation between the transparent electrodes Xa and Ya and the partition walls 35 can directly be recognized.

Steps of putting the substrates on top of each other in a method of producing the display panel according to the first 15 embodiment of the invention will now be described by reference to Figs. 10 to 13.

As shown in Fig. 10 first, the front substrate 10 with the ITO marks M1 formed thereon and the back substrate 13 with the rib marks M2 formed thereon are put on top of each other 20 (Step S1).

As the ITO marks M1 are transparent marks, the ITO marks are then irradiated with falling illumination, that is, monochromatic rays of light and as shown in Fig. 11, the interference of the light reflected from the face A of the ITO 25 mark M1 in contact with the front substrate 10 with what is

reflected from the opposite face B thereof are recognized and the coordinates of the ITO marks M1 are measured (Step S2).

When an ITO mark M1 having a film thickness of t and a refractive index of n is irradiated with monochromatic rays of light having a wavelength of λ as falling illumination 37, for example, there exists reflected light including reflected light 37A and reflected light 37B. When the falling illumination 37 is given to the front substrate 10, the reflected light 37A and the reflected light 37B intensify the strength of each other when the film thickness t of the ITO mark M1 is $\lambda/2n$ and the ITO mark M1 looks bright but weaken the strength of each other when the film thickness t of the ITO mark M1 is $\lambda/4n$ and the ITO mark M1 looks dim. Thus, the position of the ITO mark is recognizable, whereby its coordinates can be measured.

On the other hand, as the rib mark M2 is a mark low in transmittance, it is recognized by transmissive illumination in order to measure its coordinates (Step S3). When the rib mark M2 is irradiated with transmissible rays of light 38 from below the back substrate 13 as shown in Fig. 12, for example, transmitted light 38A through the back substrate 13 is high in transmittance and looks bright since the back substrate 13 is formed of glass. On the contrary, transmitted light 38B through the rib mark M2 is low in transmittance since the rib mark M2 is made of material low in transmittance such as a glass layer containing white pigments and a portion where the rib

mark M2 exists looks darker than a portion without the rib mark M2. Thus, the position of the rib mark M2 is recognizable and this allows its coordinates to be measured.

The positions of the ITO marks M1 can be recognized by
5 the falling illumination 37 and their coordinates can also be measured. On the other hand, the positions of the rib marks M2 can be recognized by the transmissive illumination 38 and their coordinates can also be measured. Therefore, as shown
in Fig. 10, the marks can be recognized and measured by
10 illuminating the marks from different directions.

With the falling illumination 37 and the transmissive illumination 38 given simultaneously or alternately, a monitor camera 36 is employed, for example, for detecting a contrast between light and shade regarding the transmitted light and
15 the reflected light so as to make the mark recognition and measure the coordinates of the marks.

More specifically, the monitor camera 36 is installed on the side of the front substrate 10 and the ITO marks M1 are recognized by coaxial falling rays of light 37 from the monitor camera 36. The rib marks M2 are recognized by the transmissive illumination 38 from the side of the back substrate 13. The mark recognition is conducted by switching these different types of illumination. For example, the mode of measuring the coordinates of the positions of the marks is controlled by using
20 the coordinates within the viewing field of the monitor camera
25

36. At this time, it is needed to predetermine the position of the monitor camera 36 and the positions of the substrates (the front substrate 10 and the back substrate 13) through robot teaching so that the presence of a mark within the viewing field 5 of the monitor camera 36 is ensured. Or absolute coordinates of the position of the monitor camera 36 are determined by reducing the position of the mark within the viewing field of the monitor camera 36 to such absolute coordinates.

Incidentally, since the mark recognition is achieved by 10 utilizing the interference of light, the use of monochromatic light is desirable for the falling illumination 37 and the transmissive illumination 38. Further, the optimum wavelength of the monochromatic light depends on the film thickness t of the ITO mark M1.

15 In case that the distance between the ITO mark M1 of the front substrate 10 and the rib mark M2 of the back substrate 13 exceeds the depth of focus of the monitor camera 36 (see Fig. 10), adjusting the focus of the monitor camera 36 on each mark is carried out.

20 The positional deviation of the coordinates of the ITO marks M1 at four corners at Step S2 from the coordinates of the rib marks M2 at four corners at Step S3 is obtained by relatively moving the front substrate 10 and the back substrate 13 whereby to optimize the positioning of the ITO marks M1 and the rib 25 marks M2 so that the positional deviation is minimized and

uniformized (Step S4).

As shown in Fig. 13, for example, given that the coordinates of the ITO mark M1 at any corner a are (M1ax and M1ay) and that the coordinates of the rib mark M2 thereat are (M2ax and M2ay),
5 a deviation Δ_a in position corresponding to the coordinates above is made obtainable by (M1ax - M2ax and M1ay - M2ay). Deviations in position at the rest of corners can similarly be obtained. Then the deviations in position corresponding to the respective coordinates are adjusted so as to minimize
10 and uniformize the deviations by moving the front substrate 10 or the back substrate 13.

In case where one or two of the ITO marks M1 and the rib marks M2 at the four corners remain unrecognizable, the rest of two or three recognizable marks may be used to place the
15 substrates on top of each other.

As described above in detail, according to the first embodiment of the invention, the transparent electrodes Xa and Ya projecting opposite to each other via the discharge gap in each cell for use in forming the display electrodes can be put
20 upon the partition wall (vertical wall 35a in particular) with excellent positional accuracy when the front substrate and the back substrate are stuck together. Thus, the performance of the PDP is prevented from being affected by the positional deviations and good display quality becomes obtainable.

25 (Second Embodiment)

A display panel according to a second embodiment of the invention is similar in structure to the PDP shown in Figs. 1 to 5 excepting the front substrate 10 (a first substrate) with first partition walls (bulked dielectric layers 11A) disposed for forming sections of transparent display electrodes X_a and Y_a as wells as display cells C in the at least first direction (the vertical direction in Fig. 1), the back substrate 13 (a second substrate) disposed separately from and opposite to the front substrate 10 with second partition walls (horizontal walls 35b) disposed for forming sections of the display cells C in the at least first direction within the display area 10a, and positioning marks (first positioning marks and second positioning marks) for use at the step of putting the substrates on top of each other.

The structure of the positioning marks for use at the step of putting the substrates on top of each other concerning the display panel according to the second embodiment of the invention will be described hereunder by reference to Figs. 8A, 8B and 14

Fig. 14 is a diagram illustrating positioning-mark forming layers of the display panel according to the second embodiment of the invention. In the display area of the PDP, a cross section taken on line from V1 up to W1 of Fig. 1 and perpendicularly folded at K shows the relation between each layer within the display area and positioning-mark forming layers formed outside

the display area.

In the display panel according to the second embodiment of the invention, bulked marks M3 (first positioning marks) are formed on the outer side 10b of the display area of the front substrate and in the same layer as a layer (e.g., a glass layer) in which the bulked dielectric layers 11A are formed within the display area 10a as shown in Fig. 14; moreover, rib marks M2 (second positioning marks) are formed on the outer side 13b of the display area of the back substrate and in the same layer as a layer in which the horizontal walls 35b extending in the line direction are formed in a position opposite to the bulked dielectric layers 11A formed within the display area 13a.

Positions where the bulked marks M3 and the rib marks M2 are formed will now be described. The positions where the bulked marks M3 are formed are similar to those described in the first embodiment of the invention and as shown in Fig. 8A, the bulked marks M3 are provided at four corners on the outer side 10b of the display area of the front substrate. In this case, no dielectric layers are formed on the respective bulked marks M3.

Further, positions where the rib marks M2 are formed are similar to those described in the first embodiment of the invention and as shown in Fig. 8B, the rib marks M2 are provided at four corners on the outer side 13b of the display area of the back

substrate.

A method of producing the display panel according to the second embodiment of the invention is similar to the method according to the first embodiment thereof and Steps S1 - S4
5 of putting the substrates on top of each other as described above are followed with the bulked marks M3 as the first positioning marks. Then the rib marks M2 forming counterparts to the bulked marks M3 are formed on the side of the back substrate 13 and when both the substrates are put on top of each other,
10 the bulked dielectric layers 11A (first partition walls) and the horizontal walls 35b (second partition walls) are properly positioned.

In this case, since the bulked marks M3 are transparent marks like the ITO marks M1, the bulked mark M3 are made
15 recognizable by the falling illumination as in the case of the ITO marks M1.

Moreover, the ITO marks M1 and the rib marks M2 as the counterparts thereto according to the first embodiment of the invention may be formed separately when the positioning of the
20 transparent display electrodes Xa and Ya and the partition walls 35 is carried out.

As set forth above in detail, according to the second embodiment of the invention, the bulked dielectric layers 11A (first partition walls) can be put upon the partition walls
25 35 (second partition walls) with excellent positional accuracy

when the front substrate 10 and the back substrate 13 are stuck together. Thus, the performance of the PDP is prevented from being affected by the positional deviations and good display quality becomes obtainable.